



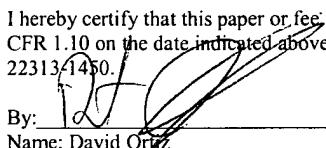
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KIDA et al. Examiner: Kiley Shawn Stoner
Serial No.: 10/616,194 Group Art Unit: 1725
Filed: July 9, 2003 Docket: 10873.1263US01
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Due Date: Date:
Title: METHOD FOR MANUFACTURING PRINTED CIRCUIT BOARD

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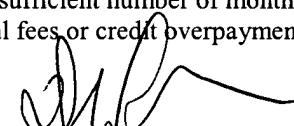
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